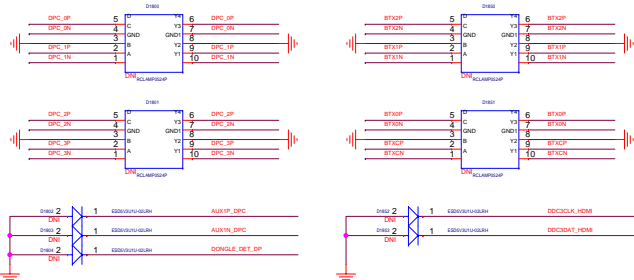
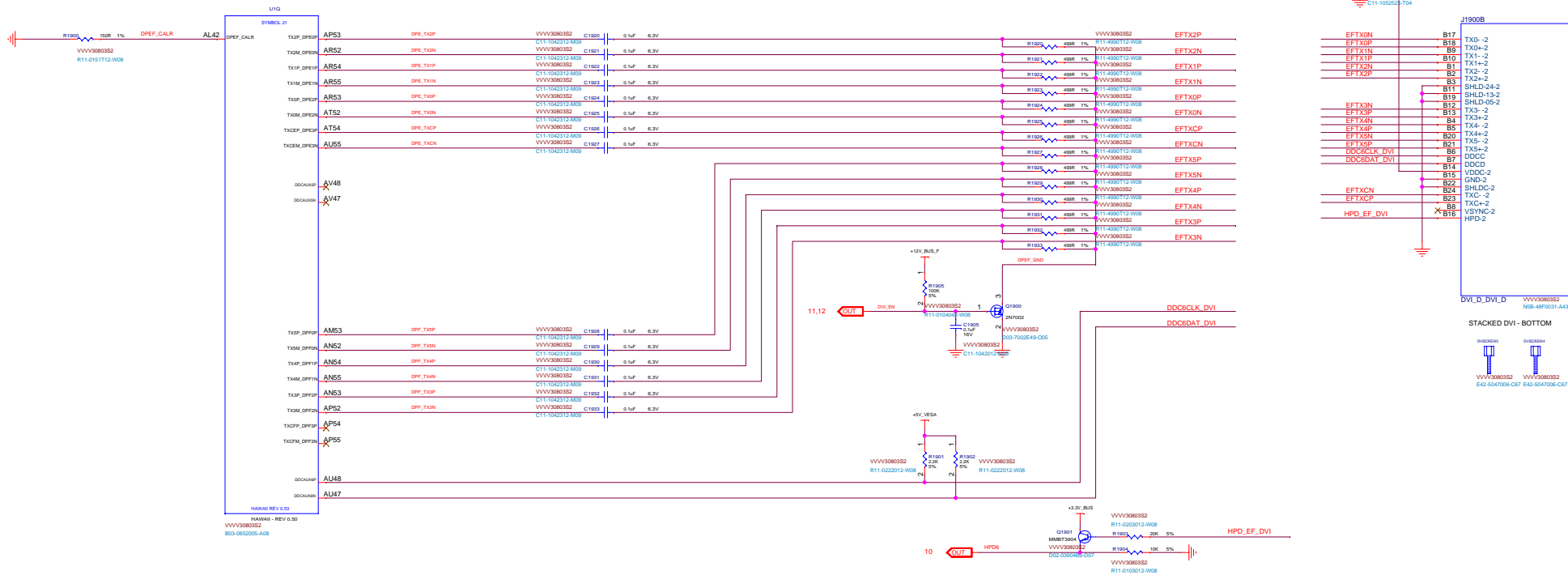
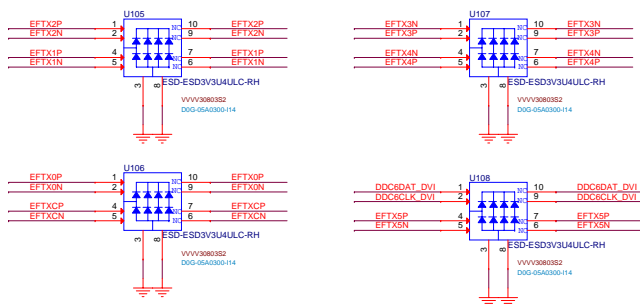


#### OPTIONAL ESD PROTECTION DIODES



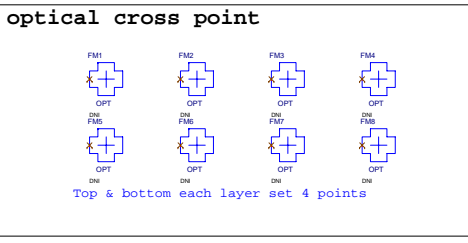


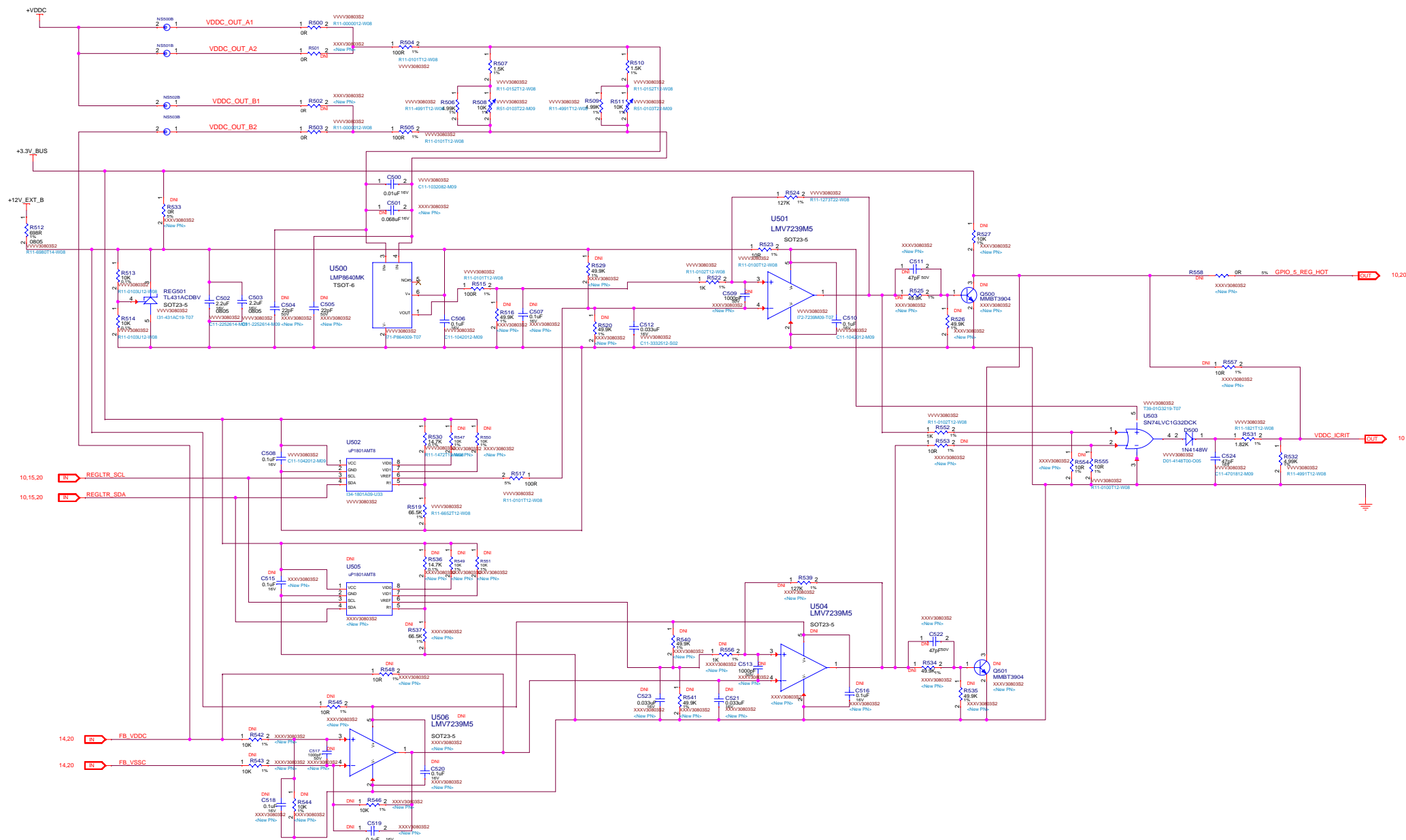
# OPTIONAL ESD PROTECTION DIODES

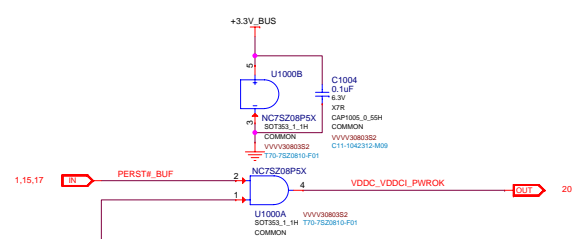
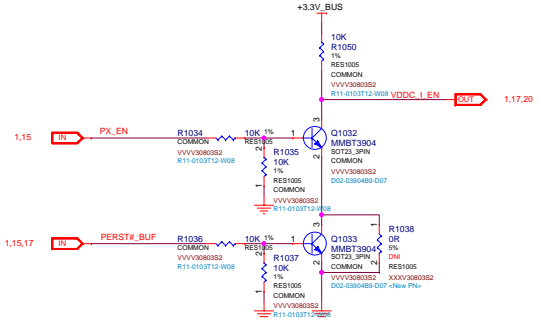
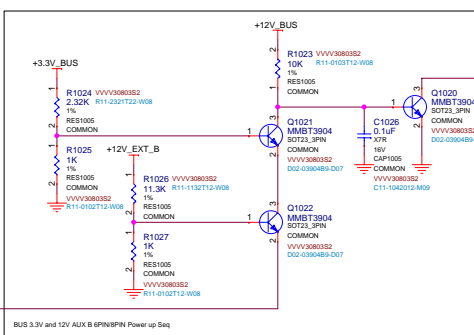
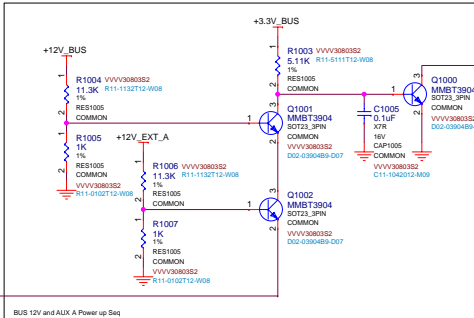
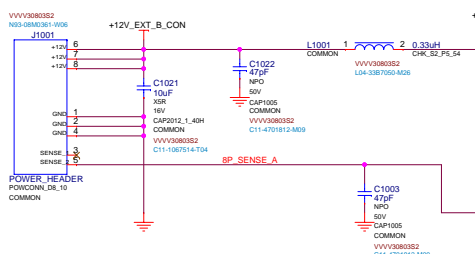
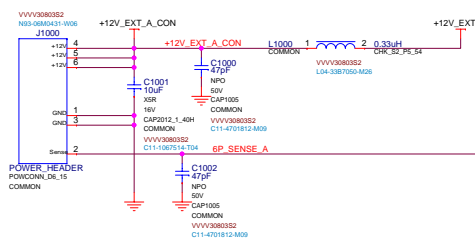
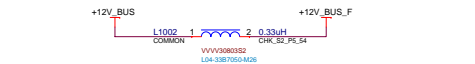




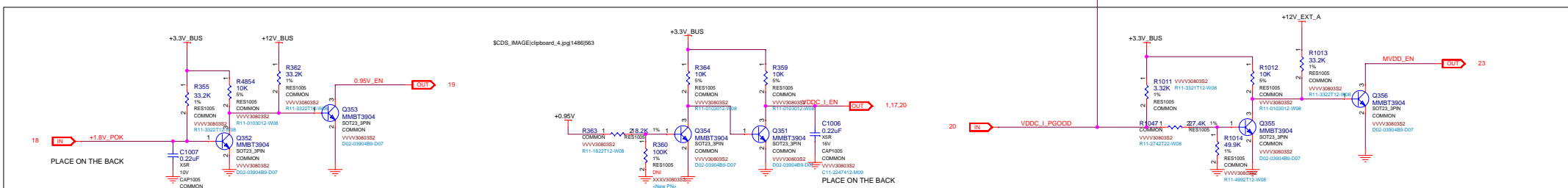




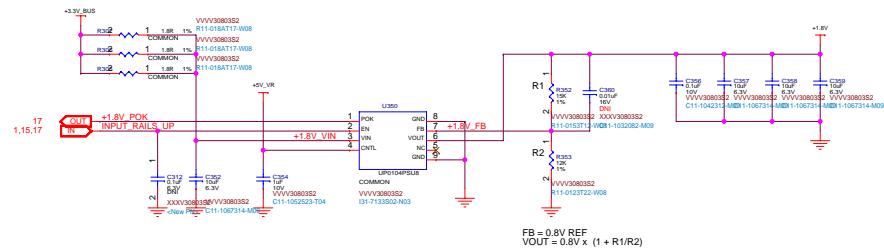




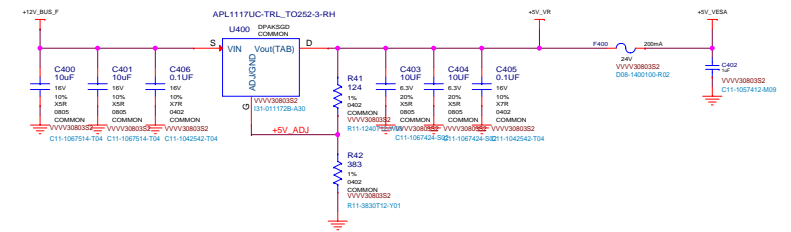
# POWER UP SEQUENCING

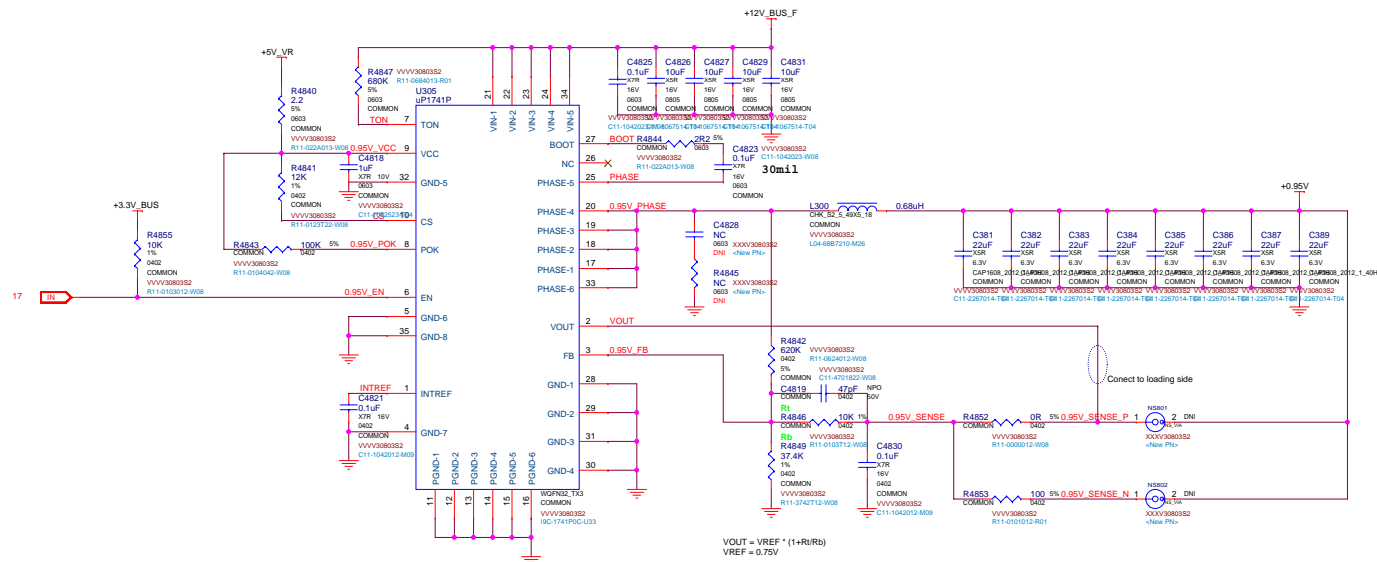


## Regulators for +1.8V



## Regulators for +5V, and +5V\_VESA

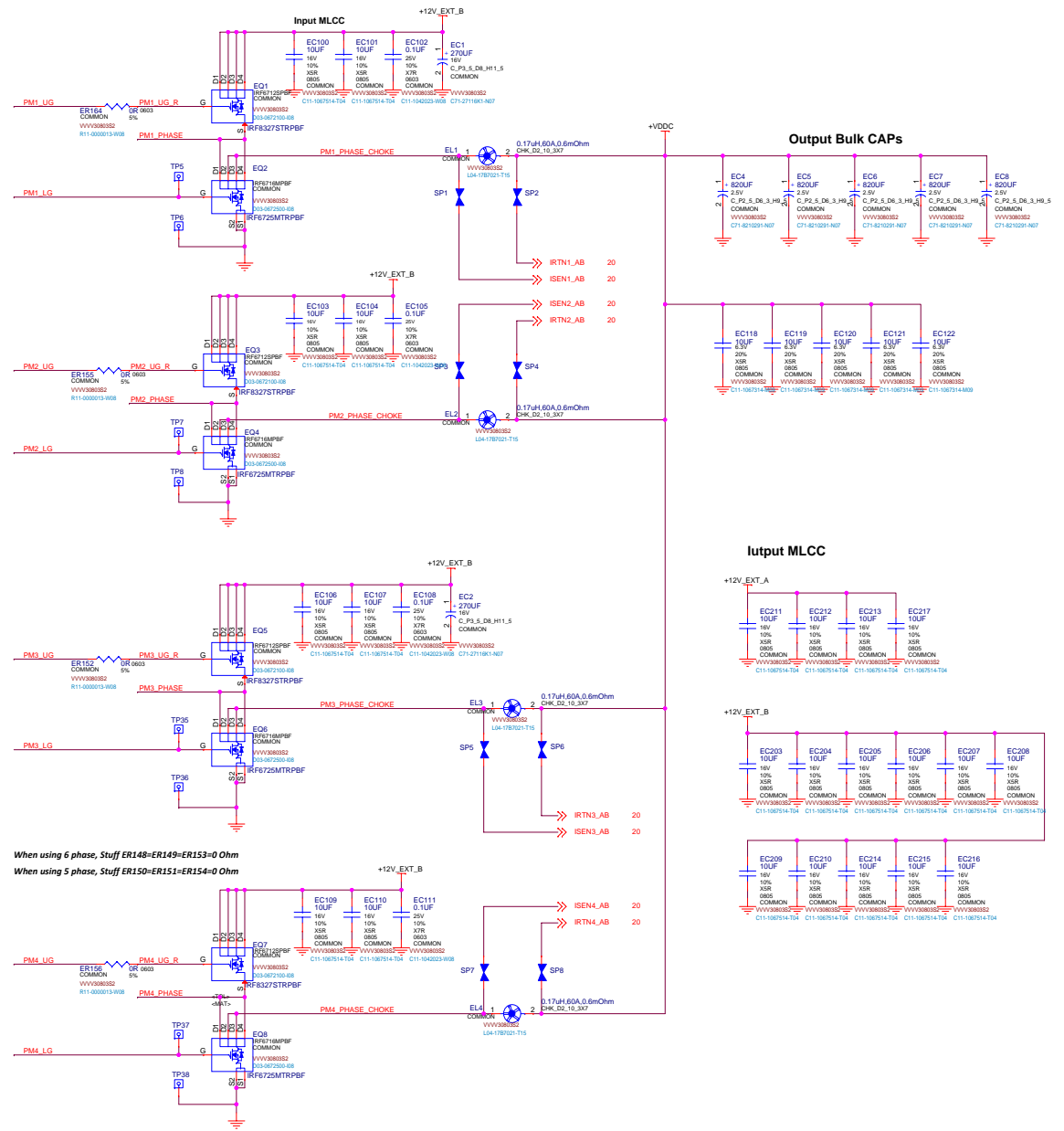
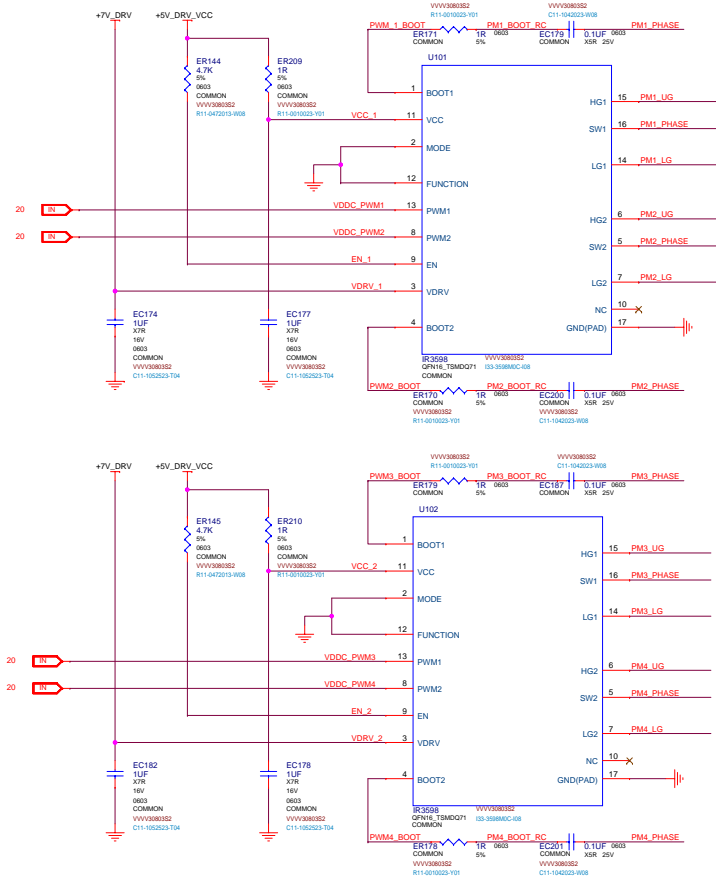
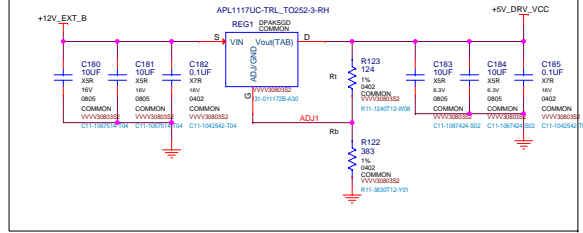






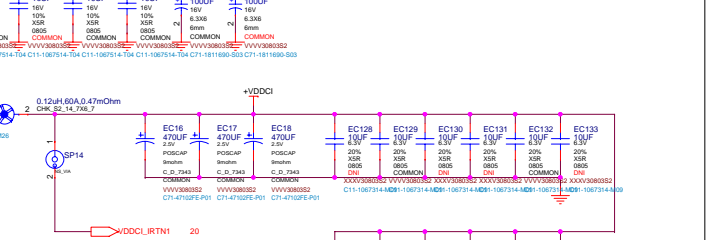
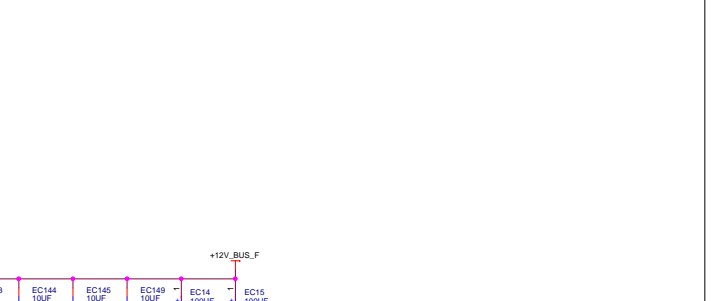
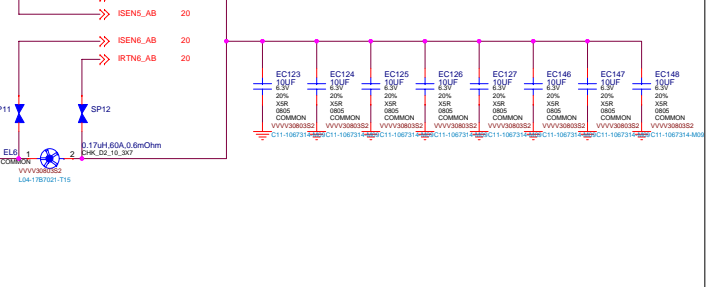
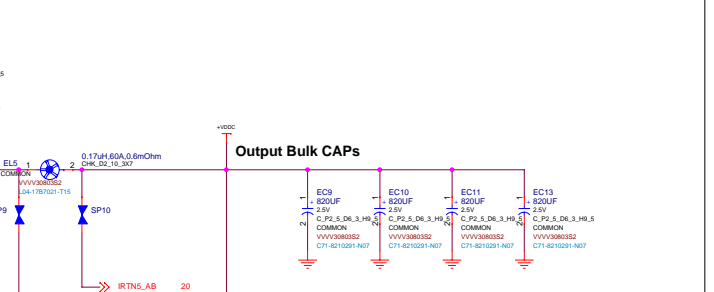
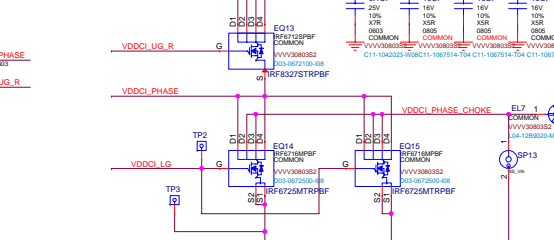
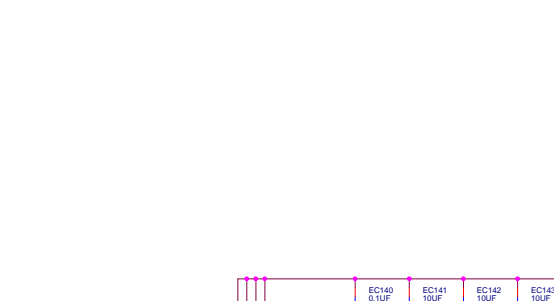
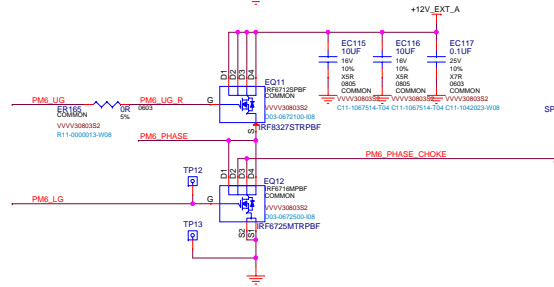
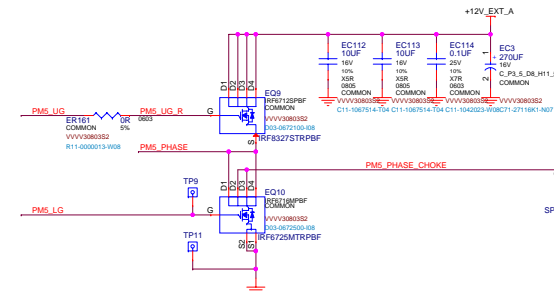
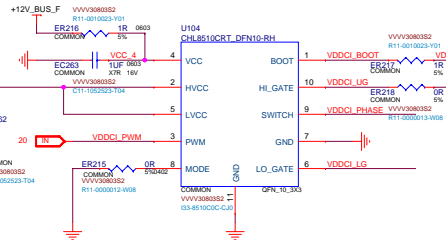
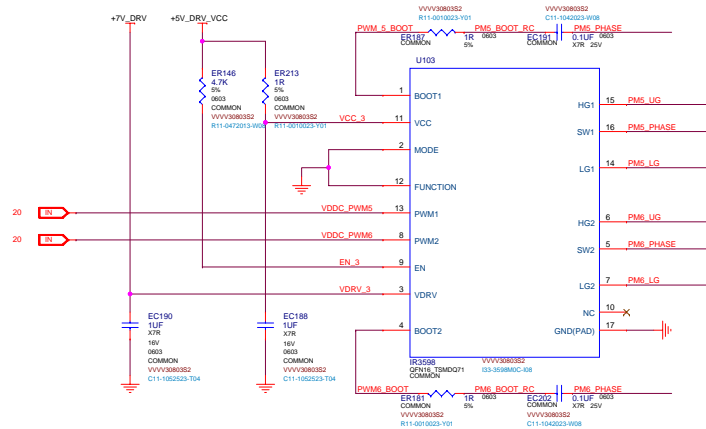
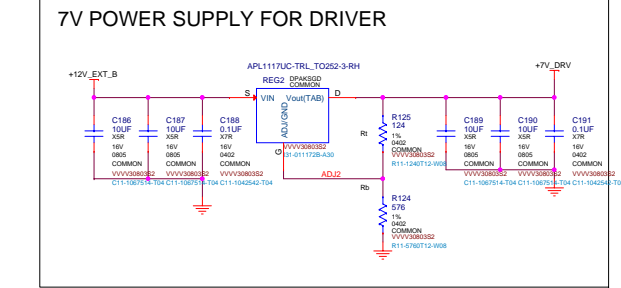


## 5V POWER SUPPLY FOR DRIVER VCC



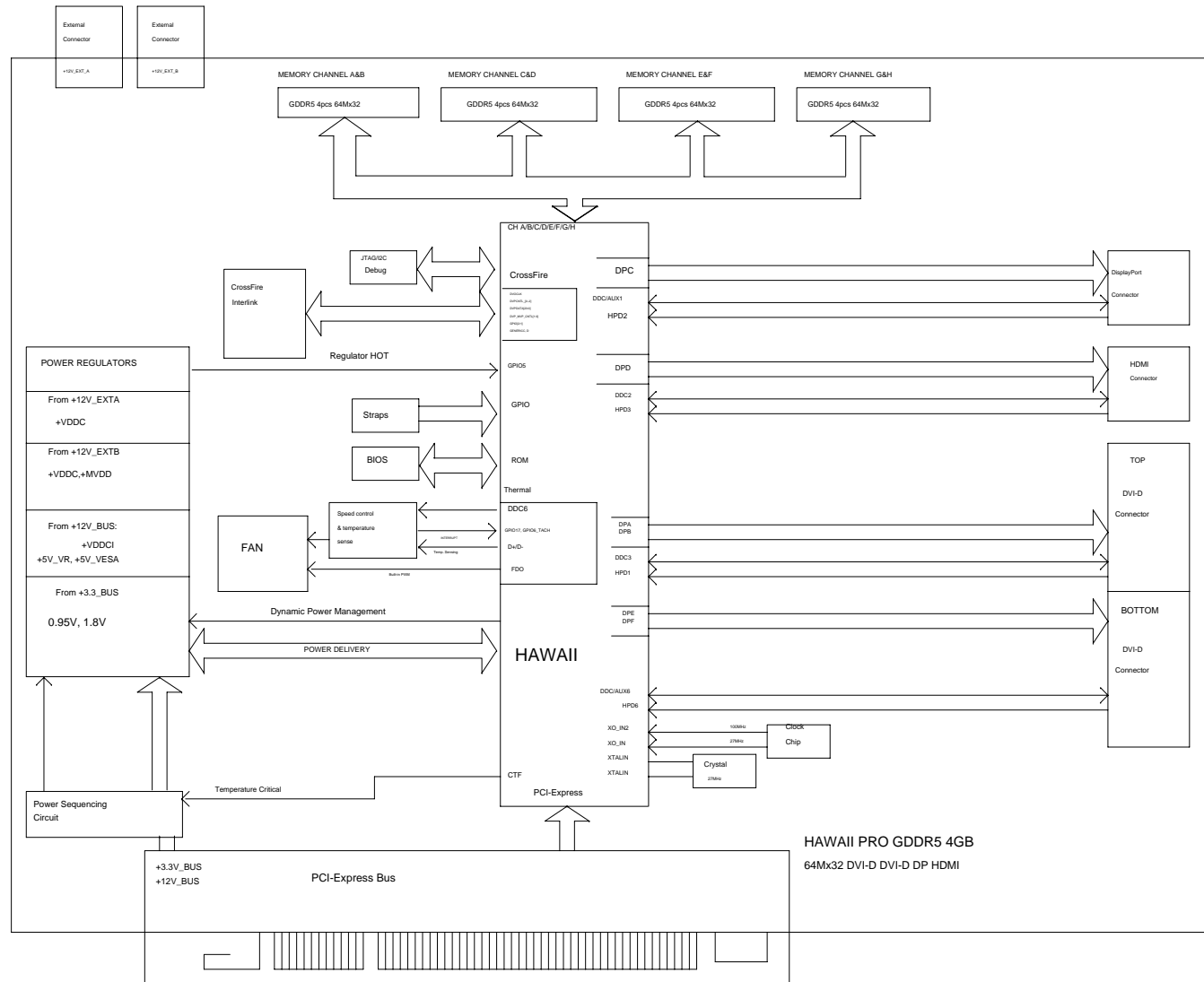
When using 6 phase, Stuff ER148-ER149-ER153=0 Ohm  
When using 5 phase, Stuff ER150-ER151-ER154=0 Ohm

# 7V POWER SUPPLY FOR DRIVER









- |   |     |               |   |
|---|-----|---------------|---|
| 1 | 00A | Feb01<br>2013 | Initial release for engineering review.   |
| 2 | 00B | Apr12<br>2013 | 1) Added provision to combine Ext_A and Ext_B on MVDD input.<br>2) Added filter on 12V_BUS.<br>3) "Fixed" CTF_DIS connection.<br>4) MVDDPWGOOD pull-up changed to 1.8V.<br>5) Removed diode from 1.8V_POK.<br>6) Improved power sequence for MVDD_EN.   |
| 3 | 00C | Jul24<br>2013 | 1) WAKEB enable changed to VDDC_I_EN.<br>2) Changed polarity of PCC comparator and added the circuitry to provide fast rising and delayed falling edges<br>3) Added filter on MVDD enable input<br>4) Added provision to connect 1.8V_DAC directly to PCC comparator.<br>5) Added extra MLCCS and polymers for whining noise reduction. |
| 4 | 00D | Sep30<br>2013 | 1) Added option for 300W board power.<br>2) Added option of PCC circuitry from C671 board.<br>3) Added VR_HOT option from C673.<br>4) Added extra filtering options for VDDC current measuring.<br>5) Added more caps on DP_VDDR.   |

- |   |         |  |         |   |
|---|---------|--|---------|---|
| 5 | V308-20 | Page01 : 1. MVDD_PWRGD change to INPUT_RAILS_UP<br>2. Remove JTAG<br>Page10 : Remove CF Test Point<br>Page11 : 1. Change DVI footprint and library<br>2. Add ESD<br>Page12 : 1. Change DP/HDMI footprint and library<br>2. Add ESD<br>Page13 : 1. Change DVI footprint and library<br>2. Add ESD<br>Page15 : 0.95V_EN change to INPUT_RAILS_UP<br>Page16 : Copy from C671 PCC circuit<br>Page17 : Copy from C671 sequence circuit<br>Page18 : 1. Change 1.8V LDO<br>2. 5V change to 1117<br>Page19 : 0.95V Change to I9C-1741P0C-U33<br>Page20 : VDDC/VDDCI controller change to IR3567B<br>Page23 : MVDD PWM change to I32-1509P0C-U33<br>Page24 : Remove Debug circuit | V308-21 | Page17 : 1. L1000/L1001/L1002/J1000/J1001 change footprint<br>2.Remove R1020/R1021/MJ1001<br>Page19 : Add 0.95_En pull-high resistor<br>Page21 : Add Ext_12V input MLCC |
|   |         |  | V308-22 | No change   |